PC250F Test Board for the A250F or A250F/NF

The PC250F is a printed circuit board designed to facilitate testing of the A250F or A250F/NF.

PC250F OPERATING NOTES

Using the A250F with the PC250F: JP1 jumper must be placed in the 2-3 position and the FET must be placed in the STORE position. (refer to layout below)

Using the A250F/NF with the PC250F: JP1 jumper must be placed in the 1-2 position and the FET must be placed in the ACTIVE Q1 position. (refer to layout below)

Refer to A250 Application Notes for testing.

PC250F Layout
PC250F Connection Diagram

- **INPUT**
  - RT 49.9
  - DET HV CAP
  - HV RES
  - -VS (-6V TYP.)

- **Output**
  - Q1 2SK152
  - A250F

- **Ground & Case**
  - GND

- **Mounting Holes**
  - OPEN

- **Components**
  - R1 100
  - C1 2.2UF
  - R2 1K
  - JP1 JUMPER
  - A250F-NF
  - OPEN

- **Other**
  - +VS (+6V TYP.)
  - A250F-NF
  - CF
  - RF